

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

PATENT

In re application of: **Miwa KOZAWA et al.**

Serial Number: **Not Yet Assigned**

Filed: **November 25, 2003**

Customer No.: 38834

For: **PROCESS FOR FORMING RESIST PATTERN, SEMICONDUCTOR
DEVICE AND FABRICATION THEREOF**

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

November 25, 2003

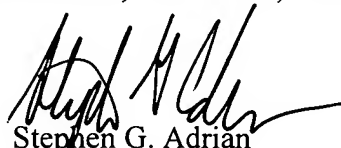
Sir:

In compliance with 37 CFR 1.56, Applicants call to the attention of the Patent and Trademark Office the references listed on the attached PTO-1449.

A copy of each of the references is enclosed herewith.

In the event there are any fees due in connection with the filing of this paper, please charge Deposit Account No. 50-2866.

Respectfully submitted,
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Enclosures: PTO-1449; References (6)

INFORMATION DISCLOSURE STATEMENT PTO-1449	Atty. Docket No. 032132	Serial No. New Appln.
	Applicant(s): Miwa KOZAWA et al.	
	Filing Date: November 25, 2003	Group Art Unit:

U.S. PATENT DOCUMENTS

Examiner Initial	Document No.	Name	Date	Class	Subclass	Filing Date (If appropriate)
_____	AA					
_____	AB					
_____	AC					
_____	AD					

FOREIGN PATENT DOCUMENTS

	Document No.	Date	Country	Translation (Yes or No)	
_____	AE	10-73927	03/17/98	Japan	Abstract. Cited in the specification
_____	AF	5-197151	08/06/93	Japan	Yes
_____	AG	2000-347414	12/15/00	Japan	Yes
_____	AH	2001-228616	08/24/01	Japan	Yes
_____	AI	2002-6491	01/09/02	Japan	Yes
_____	AJ	2002-49161	02/15/02	Japan	Yes
_____	AK				

OTHER DOCUMENTS

_____	AL	
_____	AM	
_____	AN	

Examiner	Date Considered
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